



ZXMD63P02X

DUAL 20V P-CHANNEL ENHANCEMENT MODE MOSFET

SUMMARY

$V_{(BR)DSS}=-20V$ ;  $R_{DS(ON)}=0.27\Omega$ ;  $I_D=-1.7A$

DESCRIPTION

This new generation of high density MOSFETs from Zetex utilises a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



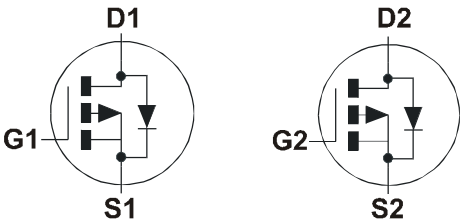
MSOP8

FEATURES

- Low on-resistance
- Fast switching speed
- Low threshold
- Low gate drive
- Low profile SOIC package

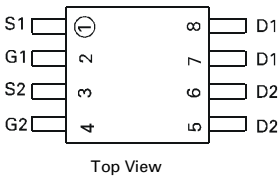
APPLICATIONS

- DC - DC Converters
- Power Management Functions
- Disconnect switches
- Motor control



ORDERING INFORMATION

DEVICE	REEL SIZE (inches)	TAPE WIDTH (mm)	QUANTITY PER REEL
ZXMD63P02XTA	7	12mm embossed	1000 units
ZXMD63P02XTC	13	12mm embossed	4000 units



DEVICE MARKING

- ZXM63P02



# ZXMD63P02X

## ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DSS}$	-20	V
Gate- Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current ( $V_{GS}=4.5V$ ; $T_A=25^\circ C$ )(b)(d) ( $V_{GS}=4.5V$ ; $T_A=70^\circ C$ )(b)(d)	$I_D$	-1.7 -1.35	A
Pulsed Drain Current (c)(d)	$I_{DM}$	-9.6	A
Continuous Source Current (Body Diode)(b)(d)	$I_S$	-1.4	A
Pulsed Source Current (Body Diode)(c)(d)	$I_{SM}$	-9.6	A
Power Dissipation at $T_A=25^\circ C$ (a)(d) Linear Derating Factor	$P_D$	0.87 6.9	W mW/ $^\circ C$
Power Dissipation at $T_A=25^\circ C$ (a)(e) Linear Derating Factor	$P_D$	1.04 8.3	W mW/ $^\circ C$
Power Dissipation at $T_A=25^\circ C$ (b)(d) Linear Derating Factor	$P_D$	1.25 10	W mW/ $^\circ C$
Operating and Storage Temperature Range	$T_j; T_{stg}$	-55 to +150	$^\circ C$

## THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)(d)	$R_{\theta JA}$	143	$^\circ C/W$
Junction to Ambient (b)(d)	$R_{\theta JA}$	100	$^\circ C/W$
Junction to Ambient (a)(e)	$R_{\theta JA}$	120	$^\circ C/W$

### NOTES

(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions

(b) For a device surface mounted on FR4 PCB measured at  $t \leq 10$  secs.

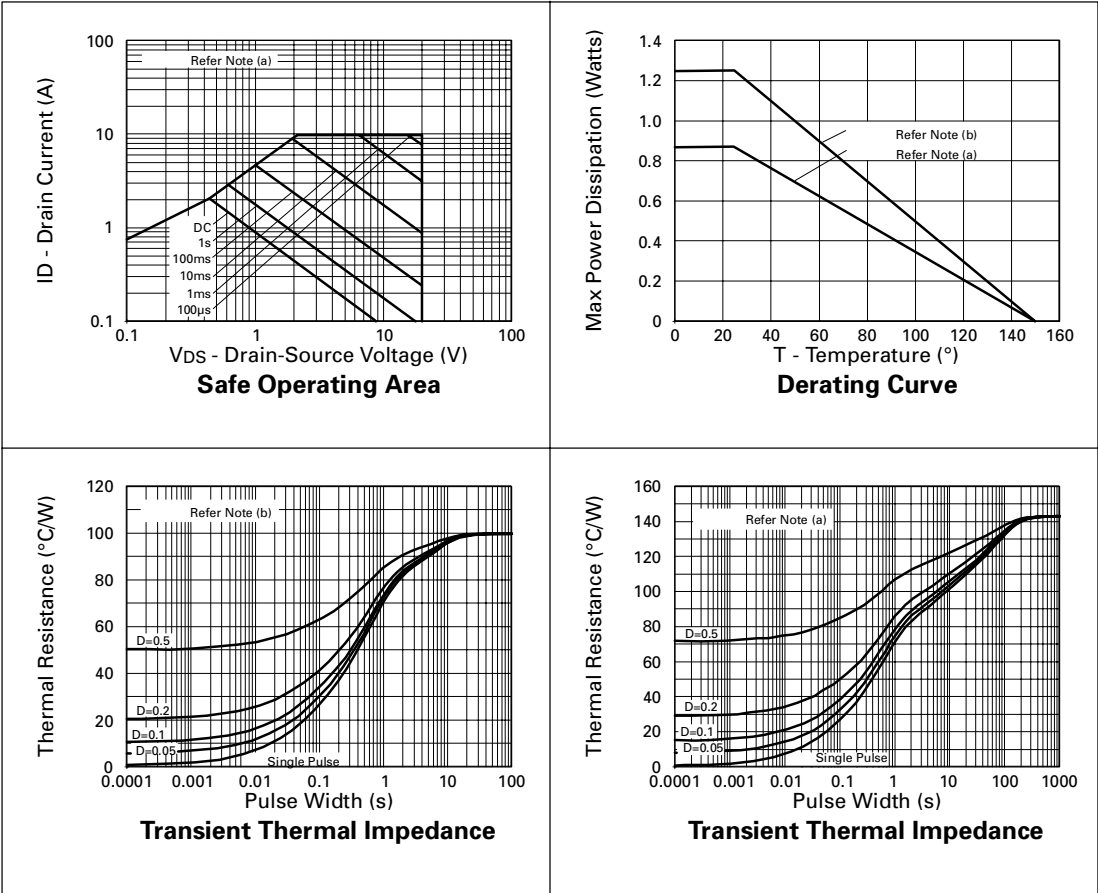
(c) Repetitive rating - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

(d) For device with one active die.

(e) For device with two active die running at equal power.

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## CHARACTERISTICS



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## ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25^{\circ}\text{C}$ unless otherwise stated).

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.
STATIC						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	-20			V	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			-1	μA	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V
Gate-Body Leakage	I <sub>GSS</sub>			±100	nA	V <sub>GS</sub> =± 12V, V <sub>DS</sub> =0V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	-0.7			V	I <sub>D</sub> =-250μA, V <sub>DS</sub> =V <sub>GS</sub>
Static Drain-Source On-State Resistance (1)	R <sub>DS(on)</sub>			0.27 0.40	Ω Ω	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.2A V <sub>GS</sub> =-2.7V, I <sub>D</sub> =-0.6A
Forward Transconductance (3)	g <sub>fs</sub>	1.3			S	V <sub>DS</sub> =-10V,I <sub>D</sub> =-0.6A
DYNAMIC (3)						
Input Capacitance	C <sub>iss</sub>		290		pF	V <sub>DS</sub> =-15 V, V <sub>GS</sub> =0V, f=1MHz
Output Capacitance	C <sub>oss</sub>		120		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>		50		pF	
SWITCHING(2) (3)						
Turn-On Delay Time	t <sub>d(on)</sub>		3.4		ns	V <sub>DD</sub> =-10V, I <sub>D</sub> =-1.2A R <sub>G</sub> =6.0Ω, R <sub>D</sub> =8.3Ω (Refer to test circuit)
Rise Time	t <sub>r</sub>		9.6		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>		16.4		ns	
Fall Time	t <sub>f</sub>		20.4		ns	
Total Gate Charge	Q <sub>g</sub>			5.25	nC	V <sub>DS</sub> =-16V,V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.2A (Refer to test circuit)
Gate-Source Charge	Q <sub>gs</sub>			1.0	nC	
Gate Drain Charge	Q <sub>gd</sub>			2.25	nC	
SOURCE-DRAIN DIODE						
Diode Forward Voltage (1)	V <sub>SD</sub>			-0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> =-1.2A, V <sub>GS</sub> =0V
Reverse Recovery Time (3)	t <sub>rr</sub>		21.7		ns	T <sub>j</sub> =25°C, I <sub>F</sub> =-1.2A, di/dt= 100A/μs
Reverse Recovery Charge(3)	Q <sub>rr</sub>		9.6		nC	

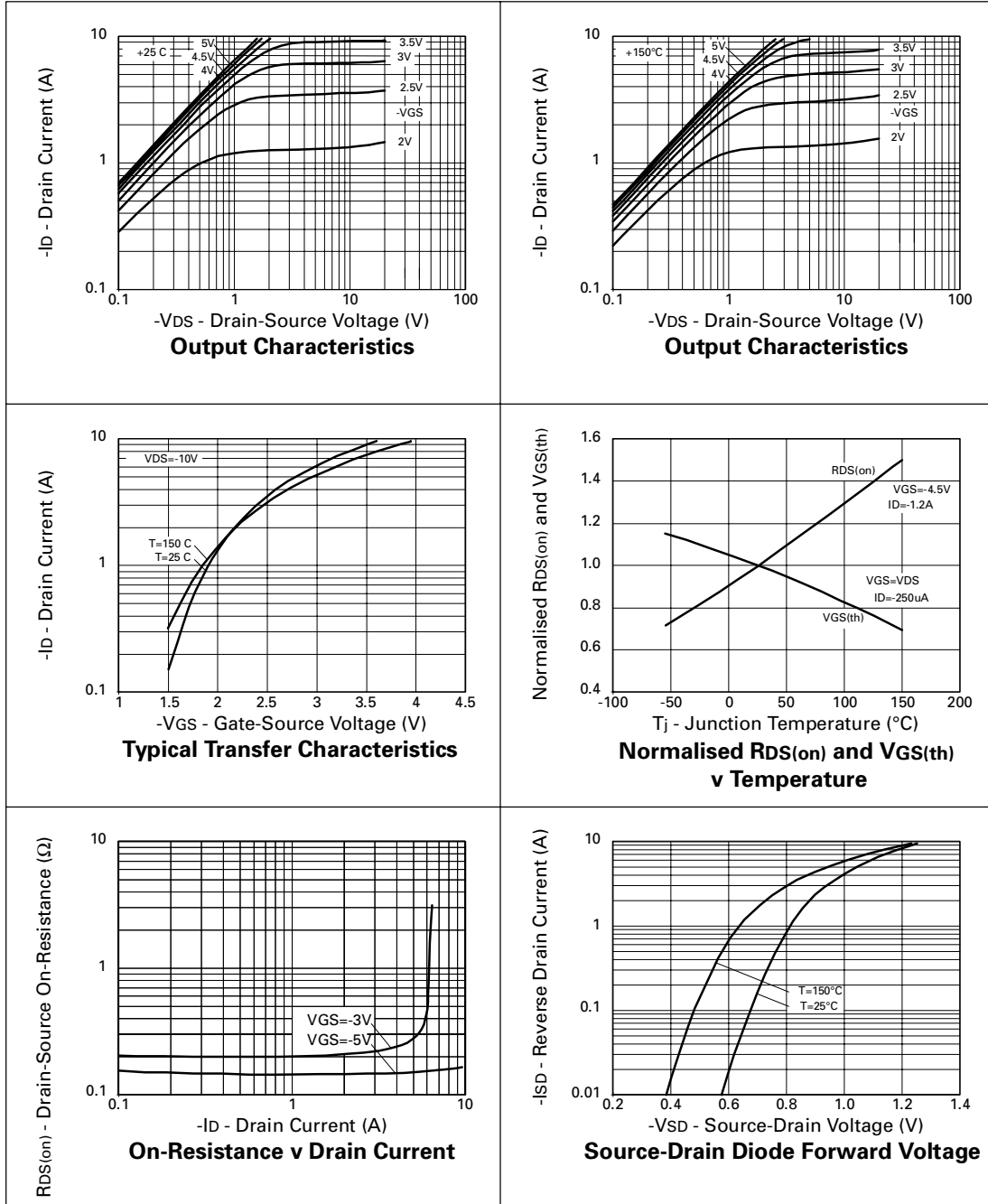
(1) Measured under pulsed conditions. Width=300 $\mu\text{s}$ . Duty cycle  $\leq 2\%$ .

(2) Switching characteristics are independent of operating junction temperature.

(3) For design aid only, not subject to production testing.

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## TYPICAL CHARACTERISTICS

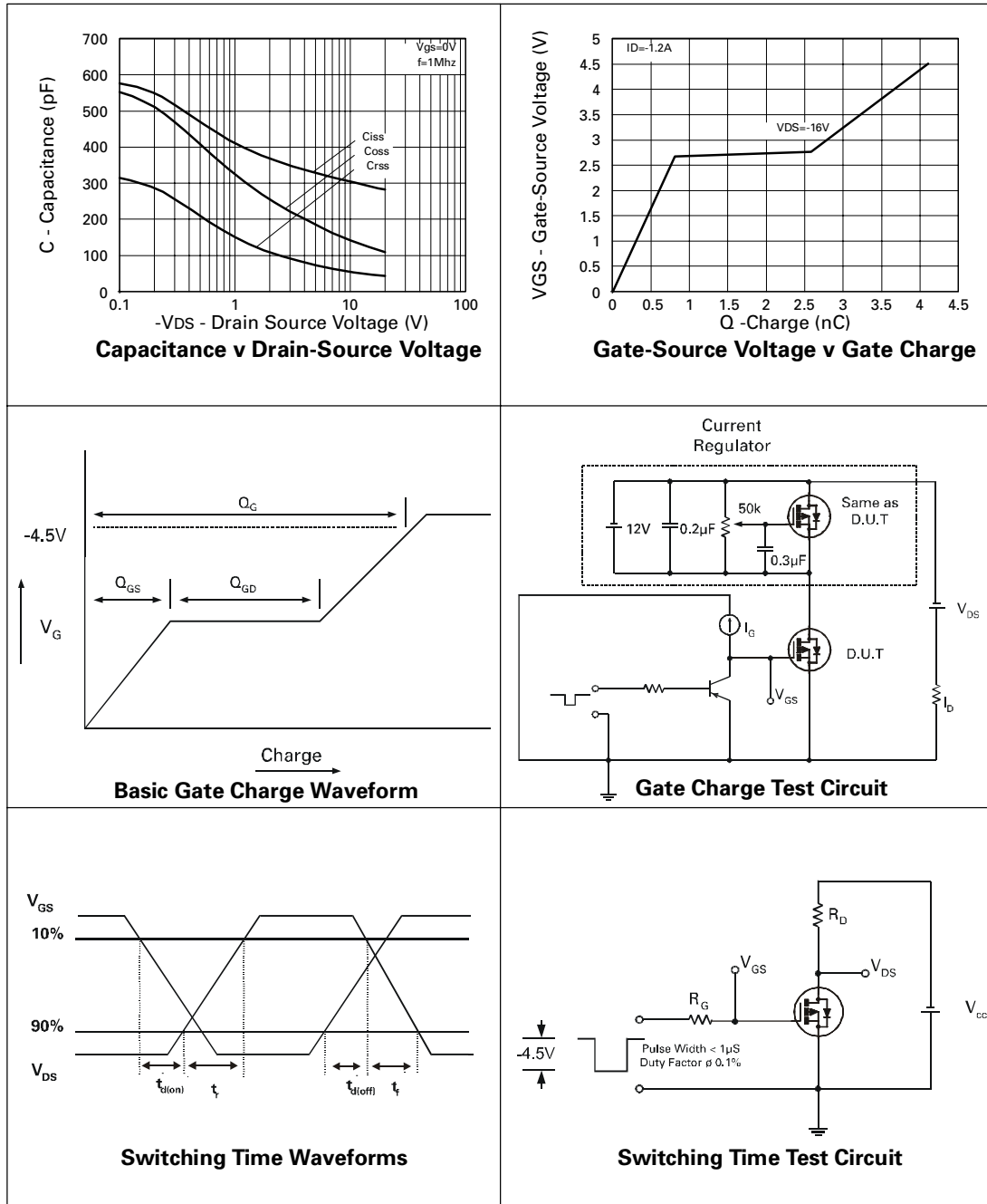


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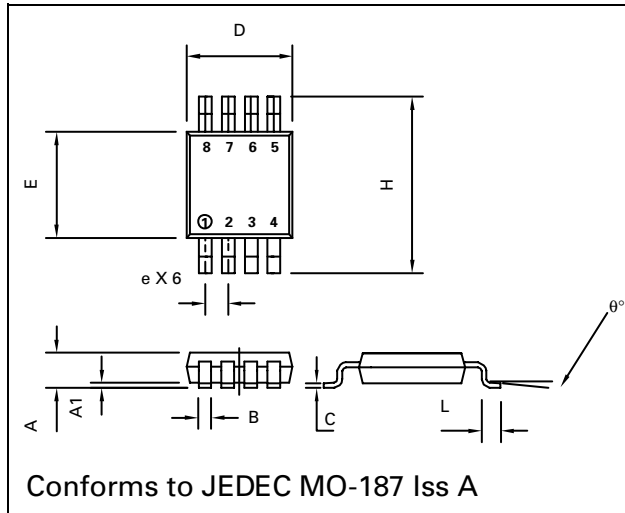
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## TYPICAL CHARACTERISTICS



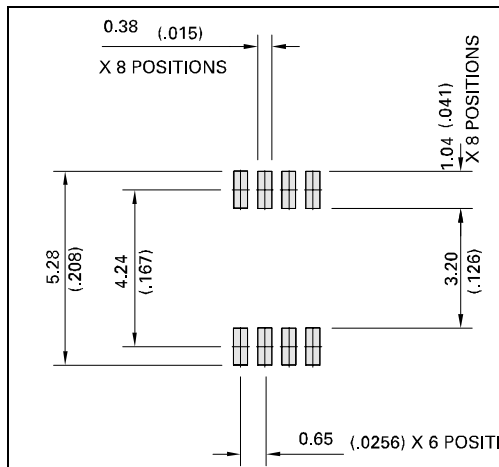
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## PACKAGE DIMENSIONS



DIM	Millimetres		Inches	
	MIN	MAX	MIN	MAX
A		1.10		0.043
A1	0.05	0.15	0.002	0.006
B	0.25	0.40	0.010	0.016
C	0.13	0.23	0.005	0.009
D	2.90	3.10	0.114	0.122
e	0.65	BSC	0.0256	BSC
E	2.90	3.10	0.114	0.122
H	4.90	BSC	0.193	BSC
L	0.40	0.70	0.016	0.028
q°	0°	6°	0°	6°

## PAD LAYOUT DETAILS



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